



IEEE CAS-INAOE Seasonal School on

Analog Design of IC & Design of Digital Systems with FPGA

Date: 09-12 July 2018

Time: 10:00 to 16:00

Location: INAOE

Prices:

\$300.00^{MX} → IEEE-CASS member

\$350.00^{MX} → IEEE member

\$500.00^{MX} → General Public

Space is limited

To reserve your seat,
please **RSVP** making the
deposit at:

Bank: Scotiabank
Titular: IEEE Sección Puebla SC
Cta: 03603631605
Clave: 044650036036316054
Suc: Forjadores Cholula

**please send your ticket and
full Name at:
jbalan@ieee.org**

*Notes:
Space for 50 people, divided between the
two workshop (25 for each one).*

ABOUT

The 2018 CAS-INAOE Seasonal School aims to offer a set of workshops on key topics in analog design and signal processing in modern and upcoming technologies. We want to promote the learning and computational methods in the area, so that the attendance can leverage their comprehension and capabilities, while also attracting new students and researches to the right problems. Some of the main topics to be covered and will divide in two workshops, which one include:

- MOSFET sizing.
- Layout design.
- CAD tutorials.
- VHDL code.
- Test bench.
- FPGA structures.

The prices include lunch, coffee break, academic resources and IEEE certificate.

For more information you can send an e-mail to:
jbalan@ieee.org

Or call to:

M.C. Oscar Jair Cinco Izquierdo: 2211467971
M.C. J. Balaam Alarcón Angulo: 2464632985

